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Patent

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NASA Case No.: NPO-20773-CU

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Inventor: Stoica, Adrian

Serial No.: 10/061,066

Filed: January 29, 2002

For: EVOLUTIONARY TECHNIQUE FOR
AUTOMATED SYNTHESIS OF
ELECTRONIC CIRCUITS

Patent Examiner: Luu, Cuong V.

Group Art Unit: 2128

RESPONSE TO OFFICE ACTION

In response to the Official Action dated November 21, 2005, the applicant hereby requests reconsideration and allowance of the application in view of the following.

Claims 1-7 and 15-27 stand rejected under 35 U.S.C. § 102(b) as unpatentable over Stoica et al., "Evolutionary Design of Electronic Devices and Circuits, Evolutionary Computation" IEEE CEC 99, July 1999. Claims 8-14 stand rejected 35 U.S.C. § 103(a) as unpatentable over Stoica et al. in view of Koza et al., "Automated Synthesis of Computational Circuits Using Genetic Programming", 13-16 April 1997, IEEE Conference.